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Docket No.: M4065.0492/P492-A  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Leonard Forbes et al.

Application No.: 10/759,107

Confirmation No.: 6955

Filed: January 20, 2004

Art Unit: 2815

For: OUTPUT PREDICTION LOGIC  
CIRCUITS WITH ULTRA-THIN  
VERTICAL TRANSISTORS AND  
METHODS OF FORMATION

Examiner: J. R. Diaz

STATUS INQUIRY

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive an Office Action from the Patent and Trademark Office.

Dated: June 8, 2005

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Gabriela I. Coman

Registration No.: 50,515

DICKSTEIN SHAPIRO MORIN &  
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant